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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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JOSEPH M POTENZA
BANNER & WITCOFF
1001 G STREET NW
WASHINGTON, DC 200014597

EXAMINER

TRAN, HAI V

ART UNIT

PAPER NUMBER

2611

DATE MAILED: 04/01/2003

14

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary

Application No.

08/932,543

Applicant(s)

KAWAKURA ET AL.

Examiner

Hai Tran

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-18 is/are pending in the application.
- 4a) Of the above claim(s) 1-4, 9 and 14 is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 5-8, 10-13 and 15-18 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on ____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. ____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) ____.
- 4) ☐ Interview Summary (PTO-413) Paper No(s) ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 01/15/03 has been entered.

Response to Arguments

Applicant's arguments with respect to claims 1-18 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

1. Claims 5-8, 10-13 and 15-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Pinder et al. (US 6105134) in view of Schiffleger (US 5434970).

Regarding claim 5, Pinder discloses an information utilization apparatus (Fig. 1) comprising:

A memory (Fig. 13; Memory 1207, ROM 1219 and non-volatile memory "NVM" 1209 Col. 21, lines 60-Col. 22, lines 11) configured to store a delivered piece of information including encoded data and applicable time data defining time period authorizing use of the encoded data (Fig. 19; Col. 33, lines 40-45);

A verification unit (Fig. 1, element 119 and Fig. 3) configured to verify whether the applicable time data included in the delivered piece of information in the memory has been falsified (by using the Global broadcast Authenticated Message "GBAM" Fig. 18-20, to determine if broadcast signal with embedded time data has been altered wherein the GBAM are hashed by one-way hash function; Col. 5, lines 54-56; Col. 9, lines 43-55; Col. 32, lines 60-Col. 33, lines 7 and lines 38-45; Col. 35, lines 28-45 and Col. 47, lines 8-60).

A decoding unit (Fig. 1, element 115 and Fig. 2B, elements 234, 236, 238) configure to decode the encoded data stored in the memory (Col. 7, lines 4-21).

A Processing unit (Fig. 12, element 1201) configured to execute an operation data decoded by the decoding unit (Fig. 12, element 1203; Col. 21, lines 55-57) and

An operation command issuing unit (Fig. 12, element 627) configured to issue a command responding to a request for the operation (Col. 21, lines 47-55) to a corresponding decoding unit (Fig. 12, el. 1203) and the processing unit (fig. 12, el. 1201) if the current time is judged by the judging unit (Fig. 3,4, 6 element 333) to be

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in the time period authorizing use of the encoded data (Col. 33, lines 42-44 and Col. 40, lines 5-11).

Pinder does not show a plurality of independently operated processing units and a plurality of decoding units wherein a plurality of independently operated processing units arranged respectively corresponding to the plurality of decoding units and configured to respectfully execute different operations on the data decoded by the plurality of decoding units.

Schiffleger discloses "a plurality of independently operated processing units (Fig. 1, element 202.1..202.N) and a plurality of decoding units (Fig. 2; Sub-Circuit 71; Col. 5, lines 1-27) wherein a plurality of independently operated processing units arranged respectively corresponding to the plurality of decoding units and configured to respectfully execute different operations on the data decoded by the plurality of decoding units (Col. 1, lines 12-22 and Col. 7, lines 55-Col. 8, lines 33). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Pinder's system with a multiprocessor system as taught by Schiffleger, so the system could improve performance by executing independent tasks of different jobs as well as related tasks of a single job (Col. 1, lines 12-19).

Regarding claim 6, Pinder discloses an information utilization apparatus (Fig. 1) comprising:

A memory (Fig. 13; Memory 1207, ROM 1219 and non-volatile memory "NVM" 1209; Col. 21, lines 60-Col.22, lines 11) configured to store a delivered piece of

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information including encoded data and applicable time data defining time period authorizing use of the encoded data (Fig. 19; Col. 33, lines 40-45);

A verification unit (Fig. 1, element 119 and Fig. 3) configured to verify whether the applicable time data included in the delivered piece of information in the memory has been falsified (by using the Global broadcast Authenticated Message "GBAM" Fig. 18-20, to determine if broadcast signal with embedded time data has been altered wherein the GBAM are hashed by one-way hash function; Col. 5, lines 54-56; Col. 9, lines 43-55; Col. 32, lines 60-Col. 33, lines 7 and lines 38-45; Col. 35, lines 28-45 and Col. 47, lines 8-60).

A decoding unit (Fig. 1, element 115 and Fig. 2B, elements 234, 236, 238) configure to decode the encoded data stored in the memory (Col. 7, lines 4-21).

A plain data storage unit configured to store the data decoded by the decoding unit (Fig. 13, element 1207; Non-Volatile Memory; Col. 21, lines 60-Col. 22, lines 11)

A Processing unit (Fig. 12, element 1201) configured to execute an operation data decoded by the decoding unit (Fig. 12, element 1203; Col. 21, lines 55-57) and

A judging unit (Fig. 3, 4, 6 element 333) configured to judge if a current time is in the time period authorizing use of the encoded data (Col. 33, lines 42-44 and Col. 40, lines 5-11) to the verified applicable time data in response to a request for an operation.

An operation command issuing unit (Fig. 12, element 627) configured to issue a command responding to a request for the operation (Col. 21, lines 47-55) to the decoding unit (Fig. 12, el. 1203) and the processing unit (fig. 12, el. 1201) if the

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current time is judged by the judging unit (Fig. 3,4, 6 element 333) to be in the time period authorizing use of the encoded data (Col. 33, lines 42-44 and Col. 40, lines 5-11) according to the verified applicable time data and the plain data storage unit does not store the decoded data (customer does not have a public key to store decoded data), and issue a command responding to the request for the operation to processing unit if the current time is judged by the judging unit to be in the time period authorizing use of the encoded data and the plain data storage unit stores the decoded data (Col. 11, lines 65-Col. 12, lines 44).

Pinder does not show a plurality of independently operated processing units configured to respectfully execute different operations on the decoded data stored by the plain data storage units.

Schiffleger discloses a plurality of independently operated processing units configured to respectfully execute different operations on the decoded data inherently stored from the main memory through the shared resource circuit 70 (Fig. 1, element 202.1...202.N; Summary; Col. 1, lines 12-22 and Col. 7, lines 55-Col. 8, lines 33). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Pinder's system with a multiprocessor system as taught by Schiffleger, so the system could improve performance of CPU time lines by executing independent tasks of different jobs as well as related tasks of a single job (Col. 1, lines 12-19).

Regarding claim 7, Pinder discloses an information utilization apparatus (Fig. 1) comprising:

A memory (Fig. 13; Memory 1207, ROM 1219 and non-volatile memory "NVM" 1209; Col. 21, lines 60-Col.22, lines 11) configured to store a delivered piece of information including encoded data and applicable time data defining time period authorizing use of the encoded data (Fig. 19; Col. 33, lines 40-45);

A verification unit (Fig.1, element 119 and Fig. 3) configured to verify whether the applicable time data included in the delivered piece of information in the memory has been falsified (by using the Global broadcast Authenticated Message "GBAM" Fig. 18-20, to determine if broadcast signal with embedded time data has been altered wherein the GBAM are hashed by one-way hash function; Col. 5, lines 54-56; Col. 9, lines 43-55; Col. 32, lines 60-Col. 33, lines 7 and lines 38-45; Col. 35, lines 28-45 and Col. 47, lines 8-60).

A Processing unit (Fig. 12, element 1201) configured to execute an operation data decoded by the decoding unit (Fig. 12, element 1203; Col. 21, lines 55-57) and

A judging unit (Fig. 3,4, 6 element 333) configured to judge if a current time is in the time period authorizing use of the encoded data (Col. 33, lines 42-44 and Col. 40, lines 5-11) to the verified applicable time data in response to a request for an operation.

An operation command issuing unit (Fig. 12, element 627) configured to issue a command responding to a request for the operation (Col. 21, lines 47-55) to the decoding unit (Fig. 12, el. 1203) and the processing unit (fig. 12, el. 1201) if the

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current time is judged by the judging unit (Fig. 3,4, 6 element 333) to be in the time period authorizing use of the encoded data (Col. 33, lines 42-44 and Col. 40, lines 5-11); and

An operation command reserving unit (DHCTSE 627) configured to prevent the issuance of a command responding to request for the operation until the time authorizing use of encoded data if the current time is judged by the judging unit not to be in the time period authorizing use of the encoded data (Col. 39, lines 17-56).

Pinder does not show a plurality of independently operated processing units and a plurality of decoding units wherein a plurality of independently operated processing units arranged respectively corresponding to the plurality of decoding units and configured to respectfully execute different operations on the data decoded by the plurality of decoding units.

Schiffleger discloses "a plurality of independently operated processing units (Fig. 1, element 202.1...202.N) and a plurality of decoding units (Fig. 2; Sub-Circuit 71; Col. 5, lines 1-27) wherein a plurality of independently operated processing units arranged respectively corresponding to the plurality of decoding units and configured to respectfully execute different operations on the data decoded by the plurality of decoding units (Col. 1, lines 12-22 and Col. 7, lines 55-Col. 8, lines 33). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Pinder's system with a multiprocessor system as taught by Schiffleger, so the system could improve performance by executing independent tasks of different jobs as well as related tasks of a single job (Col. 1, lines 12-19).

Regarding claim 8, Pinder discloses an information utilization apparatus (Fig. 1) comprising:

A memory (Fig. 13; Memory 1207, ROM 1219 and non-volatile memory "NVM" 1209; Col. 21, lines 60-Col.22, lines 11) configured to store a delivered piece of information including encoded data and applicable time data defining time period authorizing use of the encoded data (Fig. 19; Col. 33, lines 40-45);

A verification unit (Fig.1, element 119 and Fig. 3) configured to verify whether the applicable time data included in the delivered piece of information in the memory has been falsified (by using the Global broadcast Authenticated Message "GBAM" Fig. 18-20, to determine if broadcast signal with embedded time data has been altered wherein the GBAM are hashed by one-way hash function; Col. 5, lines 54-56; Col. 9, lines 43-55; Col. 32, lines 60-Col. 33, lines 7 and lines 38-45; Col. 35, lines 28-45 and Col. 47, lines 8-60).

A decoding unit (Fig. 1, element 115 and Fig. 2B, elements 234, 236, 238) configure to decode the encoded data stored in the memory (Col. 7, lines 4-21).

A plain data storage unit configured to store the data decoded by the decoding unit (Fig. 13, element 1207; Non-Volatile Memory; Col. 21, lines 60-Col. 22, lines 11)

A Processing unit (Fig. 12, element 1201) configured to execute an operation data decoded by the decoding unit (Fig. 12, element 1203; Col. 21, lines 55-57) and

A judging unit (Fig. 3,4, 6 element 333) configured to judge if a current time is in the time period authorizing use of the encoded data (Col. 33, lines 42-44 and Col.

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40, lines 5-11) to the verified applicable time data in response to a request for an operation.

An operation command issuing unit (Fig. 12, element 627) configured to issue a command responding to a request for the operation (Col. 21, lines 47-55) to the decoding unit (Fig. 12, el. 1203) and the processing unit (fig. 12, el. 1201) if the current time is judged by the judging unit (Fig. 3,4, 6 element 333) to be in the time period authorizing use of the encoded data (Col. 33, lines 42-44 and Col. 40, lines 5-11) according to the verified applicable time data and the plain data storage unit does not store the decoded data (customer does not have a public key to store decoded data), and issue a command responding to the request for the operation to processing unit if the current time is judged by the judging unit to be in the time period authorizing use of the encoded data and the plain data storage unit stores the decoded data (Col. 11, lines 65-Col. 12, lines 44); and

An operation command reserving unit (DHCTSE 627) configured to prevent the issuance of a command responding to request for the operation until the time authorizing use of encoded data if the current time is judged by the judging unit not to be in the time period authorizing use of the encoded data (Col. 39, lines 17-56).

Pinder does not show a plurality of independently operated processing units configured to respectfully execute different operations on the decoded data stored by the plain data storage units.

Schiffleger discloses a plurality of independently operated processing units configured to respectfully execute different operations on the decoded data

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inherently stored from the main memory through the shared resource circuit 70 (Fig. 1, element 202.1...202.N; Summary; Col. 1, lines 12-22 and Col. 7, lines 55-Col. 8, lines 33). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Pinder's system with a multiprocessor system as taught by Schiffleger, so the system could improve performance of CPU time lines by executing independent tasks of different jobs as well as related tasks of a single job (Col. 1, lines 12-19).

Regarding claim 10, an information access control method for use in an information utilization apparatus having a memory which stores a delivered piece of information including encoded data in which the method is analyzed with respect to claim 5.

Regarding claim 11, an information access control method for use in an information utilization apparatus having a memory which stores a delivered piece of information including encoded data in which the method is analyzed with respect to claim 6.

Regarding claim 12, an information access control method for use in an information utilization apparatus having a memory which stores a delivered piece of information including encoded data in which the method is analyzed with respect to claim 7.

Regarding claim 13, an information access control method for use in an information utilization apparatus having a memory which stores a delivered piece of

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information including encoded data in which the method is analyzed with respect to claim 8.

Regarding claim 15, a storage medium having program code instruction store thereon which perform information access control when executed by a processor in an information utilization apparatus having a memory which stores a delivered piece of information including encoded data is analyzed with respect to claim 5.

Regarding claim 16, a storage medium having program code instruction store thereon which perform information access control when executed by a processor in an information utilization apparatus having a memory which stores a delivered piece of information including encoded data is analyzed with respect to claim 6.

Regarding claim 17, a storage medium having program code instruction store thereon which perform information access control when executed by a processor in an information utilization apparatus having a memory which stores a delivered piece of information including encoded data is analyzed with respect to claim 7.

Regarding claim 18, a storage medium having program code instruction store thereon which perform information access control when executed by a processor in an information utilization apparatus having a memory which stores a delivered piece of information including encoded data is analyzed with respect to claim 8.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Chen et al. (US 5440740) shows a system and method for managing devices on multiple digital signal processors.

Chen et al. (US 4636942) shows a computer vector multiprocessing control.

Contact Fax Information

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, D.C. 20231

or Faxed to:(703) 872-9314

(for informal or draft communications, please label "PROPOSED" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA., Sixth Floor (Receptionist).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hai Tran whose telephone number is 703-308-7372. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Andrew Faile can be reached on 703-305-4380. The fax phone numbers for the organization where this application or proceeding is assigned are 703-872-9314 for regular communications and 703-872-9314 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-306-0377.

Hai Tran



Examiner
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March 23, 2003